EXAMINER'S AMENDMENT

1.   An examiner's amendment to the record appears below. Should the changes

and/or additions be unacceptable to applicant, an amendment may be filed as provided

by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be

submitted no later than the payment of the issue fee.

2.   Authorization for this examiner's amendment was given in a telephone interview

with Mr. Wade J. Brady III, Reg. No. 32,080, on 12/01/2009.

3.   Pursuant to MPEP 606.01, the title has been changed to read:

        -- TASKS DISTRIBUTION IN A MULTI-PROCESSOR INCLUDING A

TRANSLATION LOOKASIDE BUFFER SHARED BETWEEN PROCESSORS--.

4.   The following claims had been amended as:

        Claim 1. A system comprising:

        a first processor;

        a second processor coupled to the first processor, and only the first processor

configured to execute a single operating system, the second processor not executing an

operating system; ~~and~~

        a middle layer software configured to execute on the first processor and

configured to distribute tasks to run on either or both processors; and

a shared translation lookaside buffer (TLB) configured to contain a plurality of

entries in which virtual-to-physical address translations are stored, each entry also

comprising a task ID field in which a task ID associated with the corresponding

translation and with a task running on the first or second processor is stored, wherein at

least one of the shared TLB entries are invalidated, and those entries that are

invalidated have task IDs that are associated with tasks that are running or have run on

only one of the first or second processors.

Claims 6 and 10 are canceled.

Claim 13. A method usable in a multi-processor system, comprising:

executing a single operating system in the multi-processor system, the single

operating system executing on only one of a plurality of processors, the remaining ones

of the plurality of processors not executing an operating system; ~~and~~

distributing tasks to each of the plurality of processors by middle layer software

running on the processor on which the operating system executes, wherein each task

has a unique task identifier value and the method further comprises writing virtual-to-

physical address translations and task identifier values associated with the task to which

the translations pertain into a translation lookaside buffer that is shared between the

plurality of processors; and

selecting task identifier values and invalidating entries in the translation lookaside

buffer that contain the selected task identifier values and not invalidating other entries in

the translation lookaside buffer.

Claims 16 and 17 are canceled.

5.   Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Jennifer N. To whose telephone number is (571) 272-

7212. The examiner can normally be reached on M-T 6AM- 3:30 PM, F 6AM- 2:30 PM.

6.   If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Meng-Ai An can be reached on (571) 272-3756. The fax phone number for

the organization where this application or proceeding is assigned is 571-273-8300.

7.   Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

/Meng-Ai An/   /Jennifer N To/

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